

Accelerate C In Fpga

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Accelerate C In Fpga

Recent advances in C-to-FPGA design methodologies and tools facilitate the rapid creation of hardware-accelerated embedded systems. By Glenn Steiner, Kunal Shenoy, Dan Isaacs (Xilinx), and David ...

How to accelerate algorithms by automatically generating FPGA coprocessors

But when volume doesn't justify an ASIC implementation, FPGAs offer flexible hardware-based alternatives to accelerate complex algorithms. All FPGA suppliers ... (Fig. c). Up to 64 16-bit ...

FPGA Flexibility Lights A Fire Under DSPs

In the clearest confirmation to date that the " C " in " CPU " may soon no longer stand for ... specific or workload-specific acceleration. " Intel acquired FPGA accelerator maker Altera in 2015, and gave ...

Intel Shatters Precedent, Embraces Dedicated FPGA Infrastructure Processors

Embedded Supercomputing Embedded computing technology has evolved way past the point now where complete system functionality on single chip is remarkable. To ...

Chip-Level Solutions Feed AI Needs

[Anton] and [Per] are trying to fix that by building his own graphics card around an FPGA. The project is called ... all the work that into this graphics accelerator, it ' s an amazing piece ...

Open Source Graphics Card

*Silexica's technology complements our existing Vitis solution and roadmap and will accelerate ... Silexica's SLX FPGA tool suite *tackles non-synthesizable and non-hardware aware C/C++ code ...

Programmable chip maker Xilinx acquires C/C++ programming tool provider Silexica

The SLX FPGA tool suite tackles non-synthesizable C/C++ code, non-hardware-aware C/C++ code ... " Silexica ' s technology complements our existing Vitis solution and roadmap and will accelerate our ...

Xilinx acquires Silexica and its C/C++ tools

Leveraging standard high-level synthesis tools from Xilinx, the SLX FPGA tool suite tackles non-synthesizable and non-hardware aware C/C++ code ... long-term goal to accelerate the path from ...

Xilinx Acquires Silexica to Broaden its Developer Base

The Cryptographic Coprocessor (or CryptoSoc Accelerator) is a hardware IP core platform that accelerates cryptographic operations in System-on-Chip (SoC) environment on FPGA (Altera SoC ... Siliconch ...

Ting IP Listing

Accelerator RAM with ... Xilinx acquired Silexica, a C/C++ programming and analysis tools company that developed a tool suite for building applications on FPGAs and Adaptive SoCs. Xilinx will ...

Week In Review: Auto, Security, Pervasive Computing

The BittWare IA-Series of FPGA accelerators are designed to help ... Intel's Agilix technology to support three of the most prevalent accelerator form factors used for data center and network ...

BitWare Extends IA-Series of Intel® Agilix™ FPGA-based Accelerator Product Line to Address Data-Intensive Compute, Network and Storage Workloads

The latest addition to the company ' s Vivado tool suite is believed to be the first FPGA EDA tool suite based on machine ... The EDA design tool enables ML-based algorithms that accelerate design ...

Xilinx adds machine learning optimisation to Vivado to accelerate design cycle

Leveraging standard high-level synthesis tools from Xilinx, the SLX FPGA tool suite tackles non-synthesizable and non-hardware aware C/C++ code ... long-term goal to accelerate the path from ...

The Globe and Mail

Programmable devices and related technologies provider Xilinx Inc. (XLNX) has acquired Silexca, a privately-held provider of C/C++ programming ... our long-term goal to accelerate the path from ...

Xilinx Snaps up Silexica for Undisclosed Fee

Leveraging standard high-level synthesis tools from Xilinx, the SLX FPGA tool suite tackles non-synthesizable and non-hardware aware C/C++ code, detects application ... is imperative to our long-term ...

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